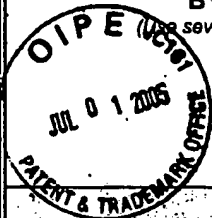


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<b>Form PTO-1449</b>			<b>ATTORNEY DOCKET NO.</b> AUS920030483US1		<b>SERIAL NO.</b> Not Assigned 10/675, 721	
<b>LIST OF PRIOR ART CITED BY APPLICANT</b> (Use several sheets if necessary)			<b>APPLICANT</b> Levine et al.			
			<b>FILING DATE</b>		<b>GROUP ART UNIT</b> Not Assigned 2191	
<b>U.S. PATENT DOCUMENTS</b>						
EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	INVENTOR NAME		CLASS/ SUBCLASS	FILING DATE
TTV	AA 5,103,394	Apr. 7, 1992	Blasciak		<del>395/575</del>	Dec. 21, 1989
TTV	AB 6,330,662 B1	Dec. 11, 2001	Patel et al.		<del>712/235</del>	Feb. 23, 1999
TTV	AC 6,480,938 B2	Nov. 12, 2002	Vondran, Jr.		<del>711/125</del>	Dec. 15, 2000
TTV	AD 6,430,741 B1	Aug. 6, 2002	Mattson, Jr. et al.		<del>717/154</del>	Feb. 26, 1999
TTV	AE 6,189,141 B1	Feb. 13, 2001	Benitez et al.		<del>717/4</del>	May 4, 1998
TTV	AF 5,930,508	Jul. 27, 1999	Faraboschi et al.		<del>395/706</del>	Jun. 9, 1997
TTV	AG 6,351,844 B1	Feb. 26, 2002	Bala		<del>717/4</del>	Nov. 5, 1998
TTV	AH 6,324,689 B1	Nov. 27, 2001	Lowney et al.		<del>717/8</del>	Sep. 30, 1998
TTV	AI 6,442,585 B1	Aug. 27, 2002	Dean et al.		<del>709/108</del>	Nov. 26, 1997
TTV	AJ 5,774,724	Jun. 30, 1998	Heisch		<del>395/704</del>	Nov. 20, 1995
TTV	AK 5,987,250	Nov. 16, 1999	Subrahmanyam		<del>395/704</del>	Aug. 21, 1997
TTV	AL 6,192,513 B1	Feb. 20, 2001	Subrahmanyam		<del>717/5</del>	Nov. 2, 1998
TTV	AM 5,691,920	Nov. 25, 1997	Levine et al.		<del>364/551.01</del>	Oct. 2, 1995
TTV	AN 6,223,338 B1	Apr. 24, 2001	Smolders		<del>717/4</del>	Sep. 30, 1998
TTV	AO 6,101,524	Aug. 8, 2000	Choi et al.		<del>709/102</del>	Oct. 23, 1997
TTV	AP 6,256,775 B1	Jul. 3, 2001	Flynn		<del>717/4</del>	Dec. 11, 1997
TTV	AQ 6,446,029 B1	Sep. 3, 2002	Davidson et al.		<del>702/186</del>	Jun. 30, 1999
TTV	AR 6,134,676	Oct. 17, 2000	VanHuben et al.		<del>714/99</del>	Apr. 30, 1998
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TTV	AT 6,243,804 B1	Jun. 5, 2001	Cheng		<del>712/228</del>	Jul. 22, 1998
TTV	AU 4,291,371	Sep. 22, 1981	Holtey		<del>364/200</del>	Jan. 2, 1979
TTV	AV 5,938,778	Aug. 17, 1999	John, Jr. et al.		<del>714/45</del>	Nov. 10, 1997
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TTV	AZ 6,070,009	May 30, 2000	Dean et al.		<del>395/704</del>	Nov. 26, 1997
TTV	BA 5,966,537	Oct. 12, 1999	Ravichandran		<del>395/709</del>	May 28, 1997
<b>DATE CONSIDERED</b> 2/16/07			<b>EXAMINER</b> TED T. VO			
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP § 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.						

<b>Form PTO-1449</b>		<b>ATTORNEY DOCKET NO.</b> AUS920030483US1		<b>SERIAL NO.</b> Not Assigned  10/675,721	
<b>LIST OF PRIOR ART CITED BY APPLICANT</b> (Use several sheets if necessary)		<b>APPLICANT</b> Levine et al.			
		<b>FILING DATE</b>		<b>GROUP ART UNIT</b> Not Assigned	
<b>U.S. PATENT DOCUMENTS</b>					
<b>EXAMINER INITIAL</b>	<b>DOCUMENT NO.</b>	<b>PUBLICATION DATE</b>	<b>INVENTOR NAME</b>	<b>CLASS/ SUBCLASS</b>	<b>FILING DATE</b>
TTU	BB 2002/0129309	Sep. 12, 2002	Floyd et al.	714/724	Dec. 18, 2000
TTU	BC 2001/0032305	Oct. 18, 2001	Barry	712/34	Feb. 23, 2001
TTU	BD 2002/0199179	Dec. 26, 2002	Lavery et al.	717/158	Jun. 21, 2001
TTU	BE 2002/0124237	Sep. 5, 2002	Sprunt et al.	717/127	Dec. 29, 2000
TTU	BF 2002/0147965	Oct. 10, 2002	Swaine et al.	717/124	Feb. 1, 2001
TTU	BG 2002/0019976	Feb. 14, 2002	Patel et al.	717/137	May 25, 2001
<b>FOREIGN PATENT DOCUMENTS</b>					
<b>EXAMINER INITIAL</b>	<b>DOCUMENT NO.</b>	<b>PUBLICATION DATE</b>	<b>COUNTRY</b>	<b>CLASS/ SUBCLASS</b>	<b>TRANSLATION YES NO</b>
TTU	BH JP2000029731	Dec. 8, 1999	Japan	G06F 9/38	X
TTU	BI JP2000347863	Dec. 15, 2000	Japan	G06F 9/38	X
<b>OTHER PRIOR ART (including author, title, date, pertinent page, etc.)</b>					
TTU	BJ	Kikuchi, "Parallelization Assist System", Joho Shori, Vol. 34, No. 9, Sept. 1993, pp. 1158-1169.			
TTU	BK	Cohen et al., "Hardware-Assisted Characterization of NAS Benchmarks", Cluster Computing, Vol. 4, No. 3, July 2001, pp. 189-196.			
TTU	BL	Talla et al., "Evaluating Signal Processing and Multimedia Applications on SIMD, VLIW and Super Scalar Architectures", International Conference on Computer Design, Austin, Sept. 17-20, 2000, pp. 163-172.			
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TTU	BN	Talla et al., "Execution Characteristics of Multimedia Applications on a Pentium II Processor", IEEE International Performance, Computing, and Communications Conference, 19 <sup>th</sup> , Phoenix, Feb. 20-22, 2000, pp. 516-524.			
TTU	BO	IBM Research Disclosure Bulletin 444188, "Enable Debuggers as an Objective Performance Measurement Tool for Software Development Cost Reduction", April 2001, pp. 686-688.			
<b>RELATED PATENT APPLICATIONS</b>					
<b>EXAMINER INITIAL</b>	<b>APPLICATION NO./ ATTY. DOCKET NO.</b>	<b>APPLICANT</b>	<b>TITLE</b>	<b>FILING DATE</b>	
TTU	BP 09/435,069 AT9-99-491	Davidson et al.	Method and Apparatus for Instruction Sampling for Performance Monitoring and Debug	Nov. 4, 1999	
TTU	BQ 08/538,071 AUT919950055US1	Gover et al.	Method and System for Selecting and Distinguishing an Event Sequence using an Effective Address in a Processing System	Oct. 2, 1995	
<b>DATE CONSIDERED</b> 2/16/07		<b>EXAMINER</b> TED T. VO			
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Form PTO-1449		ATTORNEY DOCKET NO. AUS920030483US1		SERIAL NO. 10/675,721	
<b>LIST OF PRIOR ART CITED BY APPLICANT</b> <small>(Use several sheets if necessary)</small>		APPLICANT Levine et al.			
		FILING DATE September 30, 2003		GROUP ART UNIT 2191	
<b>RELATED PATENT APPLICATIONS</b>					
EXAMINER INITIAL		APPLICATION NO / ATTY. DOCKET NO.	APPLICANT	TITLE	FILING DATE
TTL	AA	10/675,777 / AUS920030477US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Instruction Execution and Data Accesses	Sep. 30, 2003
TTL	AB	10/674,604 / AUS920030478US1	DeWitt, Jr. et al.	Method and Apparatus for Selectively Counting Instructions and Data Accesses	Sep. 30, 2003
TTL	AC	10/675,831 / AUS920030479US1	DeWitt, Jr. et al.	Method and Apparatus for Generating Interrupts Upon Execution of Marked Instructions and Upon Access to Marked Memory Locations	Sep. 30, 2003
TTL	AD	10/675,778 / AUS920030480US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Data Accesses and Instruction Executions that Exceed a Threshold	Sep. 30, 2003
TTL	AE	10/675,776 / AUS920030481US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Execution of Specific Instructions and Accesses to Specific Data Locations	Sep. 30, 2003
TTL	AF	10/675,751 / AUS920030482US1	Levine et al.	Method and Apparatus for Debug Support for Individual Instructions and Memory Locations	Sep. 30, 2003
TTL	AG	10/674,642 / AUS920030484US1	Levine et al.	Method and Apparatus to Autonomically Count Instruction Execution for Applications	Sep. 30, 2003
TTL	AH	10/674,606 / AUS920030485US1	Levine et al.	Method and Apparatus to Autonomically Take an Execution on Specified Instructions	Sep. 30, 2003
TTL	AI	10/675,783 / AUS920030486US1	Levine et al.	Method and Apparatus to Autonomically Profile Applications	Sep. 30, 2003
TTL	AJ	10/675,872 / AUS920030487US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Instruction and Memory Location Ranges	Sep. 30, 2003
DATE CONSIDERED		EXAMINER			
TTL		TED			
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# INFORMATION DISCLOSURE STATEMENT BY APPLICANT

## Complete if Known

Application Number 10/675,721  
Filing Date 09/30/2003  
First Named Inventor Levine et al.  
Examiner Name Ghan, Eddie P.  
Art Unit 2191  
Attorney Docket No. AUS920030483US1

Examiner Initials	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
TTU	AA 4,034,353	07-1977	Denny et al.	
TTU	AB 4,821,178	04-1989	Levin et al.	
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TTU	AV 4,825,359	04-1989	Ohkami et al.	

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Examiner Initial	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

## NON PATENT LITERATURE

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TTU	AW Mano, "Computer System Architecture", Prentice-Hall, Inc., 2 <sup>nd</sup> Ed., 1982, pp.434-443.
TTU	AX Inoue, "Digital mobile communication system designed for nationwide police activities - WIDE system", IEEEExplore, October 1996, pp. 1-2.

Examiner:

TED T. VO

Date Considered:

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Application Number	10/675,721
Filing Date	09/30/2003
First Named Inventor	Levine et al.
Examiner Name	<del>Nahar Gammoo</del>
Art Unit	2191
Attorney Docket No.	AUS920030483US1

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## FOREIGN PATENT DOCUMENTS

Examiner Initial	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

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**INFORMATION DISCLOSURE  
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**Complete if Known**

Application Number	10/675,721
Filing Date	09/30/2003
First Named Inventor	Levine et al.
Examiner Name	Nahar Gamrun
Art Unit	2191
Attorney Docket No.	AUS920030483US1

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Examiner Initial	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

<b>Examiner Initial</b>	Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	
TH/AC	Short, "Embedded Microprocessor Systems Design: An Introduction Using the Intel 80C188EB" Prentice-Hall, Inc.: 1998, p. 761.	

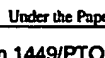
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# INFORMATION DISCLOSURE STATEMENT BY APPLICANT

**Complete If Known**

Application Number	10/675,721
Filing Date	09/30/2003
First Named Inventor	Levine et al.
Examiner Name	<del>Nehor-Gamrun</del>
Art Unit	2191
Attorney Docket No.	AUS920030483US1

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# FOREIGN PATENT DOCUMENTS

Examiner Initial	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

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TTC	AH	TANENBAUM, "Structured Computer Organization", 1984, Prentice-Hall, Inc., 2 <sup>nd</sup> Edition, pp. 10-12.
TLC	AI	TORRELLAS et al., "False Sharing and Spatial Locality in Multiprocessor Caches", June 1994, IEEE Transactions on Computers, Vol. 43, No. 6, pp. 651-663.
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Application Number	10/675,721
Filing Date	09/30/2003
First Named Inventor	Levine et al.
Examiner Name	Nehor-Gemrun
Art Unit	2191
Attorney Docket No.	AUS920030483US1

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FL	AL	JEONG et al., "Cost Sensitive Cache Replacement Algorithms", June 2002, Second Workshop on Caching, Coherence and Consistency, NY, pp. 1-14.

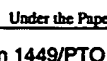
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Application Number	10/675,721
Filing Date	09/30/2003
First Named Inventor	Levine et al.
Examiner Name	Nehor-German
Art Unit	2191
Attorney Docket No.	AUS920030483US1

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## FOREIGN PATENT DOCUMENTS

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## NON PATENT LITERATURE

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**Examiner:**

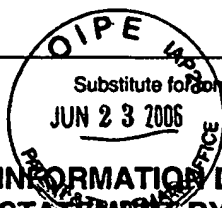
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Application Number	10/675,721
Filing Date	09/30/2003
First Named Inventor	Levine et al.
Examiner Name	<del>Natalie G. Gorman</del>
Art Unit	2191
Attorney Docket No.	AUS920030483US1

Examiner Initials	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
TL	AA 6,149,318	11-21-2000	Chase et al.	
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TL	AD 6,460,135 B1	10-01-2002	Suganuma	
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Examiner Initial	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

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# INFORMATION DISCLOSURE STATEMENT BY APPLICANT

## Complete if Known

Application Number 10/675,721  
 Filing Date 09/30/2003  
 First Named Inventor Levine et al.  
 Examiner Name ~~Nahar Gantun~~  
 Art Unit 2191  
 Attorney Docket No. AUS920030483US1

Examiner Initials	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
TL	AA	5,857,097	01-05-1999	Henzinger et al.
TL	AB	6,820,155	11-16-2004	Ito
TL	AC	5,745,770	04-28-1998	Thangadurai et al.

## FOREIGN PATENT DOCUMENTS

Examiner Initial	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

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TL AD	MERTEN et al., "A Hardware Driven Profiling Scheme for Identifying Program Hot Spots to Support Runtime Optimization", IEEE, 1999, pp. 136-147.

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Date Considered:

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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> (Not for submission under 37 CFR 1.99)	Application Number		10675721	
	Filing Date		2003-09-30	
	First Named Inventor		Levine et al.	
	Art Unit		2191	
	Examiner Name		<del>Nehar, Gannan</del>	
	Attorney Docket Number		AUS920030483US1	

U.S. PATENTS							Remove	
Examiner Initial*	Cite No	Patent Number	Kind Code <sup>1</sup>	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear		
TTL	1	6944720	B2	2005-09-13	Sperber et al.			
TTL	2	7035996	B2	2006-04-25	Woodall et al.			
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Examiner Initial*	Cite No	Publication Number	Kind Code <sup>1</sup>	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear		
TTL	1	20040030870	A1	2004-02-12	Buser			
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Examiner Initial*	Cite No	Foreign Document Number <sup>3</sup>	Country Code <sup>2</sup> i	Kind Code <sup>4</sup>	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear	T <sup>5</sup>
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Application Number	10675721
Filing Date	2003-09-30
First Named Inventor	<del>Levine et al.</del> LEVINE et al
Art Unit	2191
Examiner Name	<del>Manar Qamran</del>
Attorney Docket Number	AUS920030483US1

Examiner Initials*	Cite No	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, pages(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>5</sup>
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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> ( Not for submission under 37 CFR 1.99)	Application Number		10675721	
	Filing Date		2003-09-30	
	First Named Inventor		Levine et al.	
	Art Unit		2191	
	Examiner Name		Nahar, Gauran	
	Attorney Docket Number		AUS920030483US1	

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Examiner Initial*	Cite No	Patent Number	Kind Code <sup>1</sup>	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear		
TTL	1	6973417	B1	2005-12-06	Maxwell et al.			
TTL	2	6721875	B1	2004-04-13	McCormick et al.			
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Application Number	10675721
Filing Date	2003-09-30
First Named Inventor	Levine et al.
Art Unit	2191
Examiner Name	Neher, Camrun
Attorney Docket Number	AUS920030483US1

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	Art Unit	2191	
	Examiner Name	Nahar, Qamran	
	Attorney Docket Number	AUS920030483US1	

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Examiner Initial*	Cite No	Patent Number	Kind Code <sup>1</sup>	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear	
TTL	1	5966538		1999-10-12	Granston et al.		
TTL	2	5940618		1999-08-17	Blandy et al.		
TTL	3	6189142	B1	2001-02-13	Johnston et al.		
TTL	4	5768500		1998-06-16	Agrawal et al.		
TTL	5	6026235		2000-02-15	Shaughnessy		
TTL	6	5950003		1999-09-07	Kaneshiro et al.		
TTL	7	6594820	B1	2003-07-15	Ungar		
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Application Number	10675721
Filing Date	2003-09-30
First Named Inventor	Levine et al.
Art Unit	2191
Examiner Name	Nahar, Qamrun
Attorney Docket Number	AUS920030483US1

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TTU	1	20020073406	A1	2002-06-13	Gove	
TTU	2	20030066055	A1	2003-04-03	Spivey	
TTU	3	20030131343	A1	2002-07-10	French et al.	
TTU	4	20040268316	A1	2004-12-30	Fisher et al.	

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Application Number	10675721
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First Named Inventor	Levine et al.
Art Unit	2191
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Attorney Docket Number	AUS920030483US1

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	Filing Date		2003-09-30
	First Named Inventor	Levine et al.	
	Art Unit	2191	
	Examiner Name	Nahar, Qamran	
	Attorney Docket Number	AUS920030483US1	

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TTU	1	5761103		1998-06-02	Oakland et al.		
TTU	2	6549959	B1	2003-04-13	Yates et al.		
TTU	3	6772322	B1	2004-08-03	Merchant et al.		
TTU	4	5937437		1999-08-10	Roth et al.		
TTU	5	6070009		2000-05-30	Dean et al.		
TTU	6	6381679	B1	2002-04-30	Matsubara et al.		
TTU	7	6687794	B2	2004-02-03	Malik		
TTU	8	6539458	B2	2003-03-25	Holmberg		

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First Named Inventor	Levine et al.
Art Unit	2191
Examiner Name	Nahar, Qamrun
Attorney Docket Number	AUS920030483US1

FTU	9	6973542	B1	2005-12-06	Schmuck et al.	
FTU	10	6782454	B1	2004-08-24	Damron	
FTU	11	5652858		1997-06-29	Okada et al.	
FTU	12	6848029	B2	2005-01-25	Coldewey	
FTU	13	5822790		1998-10-13	Mehrotra	

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Application Number	10675721
Filing Date	2003-09-30
First Named Inventor	Levine et al.
Art Unit	2191
Examiner Name	Nahar, Qamrun
Attorney Docket Number	AUS920030483US1

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TTL✓	1	"Method for the dynamic prediction of nonsequential memory accesses", September 25, 2002, pp. 1-4, ip.com IPCOM000009888D	<input type="checkbox"/>
TTL✓	2	"Cache Miss Director - A Means of Prefetching Cache Missed Lines", August 1, 1982, IBM Technical Disclosure Bulletin, Vol. 25, Issue 3A, pp.1286	<input type="checkbox"/>

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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> ( Not for submission under 37 CFR 1.99)	Application Number		10675721
	Filing Date		2003-09-30
	First Named Inventor	Levine et al.	
	Art Unit	2191	
	Examiner Name	Nahar, Qamrun	
	Attorney Docket Number	AUS920030483US1	

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Examiner Initial*	Cite No	Patent Number	Kind Code <sup>1</sup>	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
TTL	1	5657253		1997-08-12	Dreyer et al.	
TTL	2	5991708		1999-11-23	Levine et al.	
TTL	3	5564015		1996-10-08	Bunnell	
TTL	4	5450349		1995-09-12	Brown et al.	
TTL	5	5987598		1999-11-16	Levine et al.	
TTL	6	6009514		1999-12-18	Henzinger et al.	
TTL	7	6112317		2000-08-29	Berc et al.	
TTL	8	5896538		1999-04-20	Blandy et al.	

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Application Number	10675721
Filing Date	2003-09-30
First Named Inventor	Levine et al.
Art Unit	2191
Examiner Name	Nahar, Qamrun
Attorney Docket Number	AUS920030483US1

TTU	9	5355487		1994-10-11	Keller et al.	
TTU	10	6948032	B2	2005-09-20	Kadambi et al.	

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TTU	1	"CPU Cache", Wikipedia, pp. 1-14 retrieved 11/1/06 <a href="http://en.wikipedia.org/wiki/CPU_cache">http://en.wikipedia.org/wiki/CPU_cache</a>	<input type="checkbox"/>
TTU	2	AMMONS et al., "Exploiting Hardware Performance Counters with Flow and Context Sensitive Profiling", 1997, ACM, pp. 85-96.	<input type="checkbox"/>



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Examiner Initial*	Cite No	Patent Number	Kind Code <sup>1</sup>	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
TTU	1	6105129		2000-08-15	Meier et al.	
TTU	2	6928521	B1	2005-08-09	Burton et al.	
TTU	3	6598153	B1	2003-07-22	Flachs et al.	
TTU	4	5875294		1999-02-23	Roth et al.	
TTU	5	6073109		2000-06-06	Flores et al.	
TTU	6	6871298	B1	2005-03-22	Cavanaugh et al.	
TTU	7	5555432		1996-09-10	Hinton et al.	
TTU	8	6145077		2000-11-07	Sidwell et al.	

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TU	1	6105129		2000-08-15	Meier et al.		
TU	2	6928521	B1	2005-08-09	Burton et al.		
TU	3	6598153	B1	2003-07-22	Flachs et al.		
TU	4	5875294		1999-02-23	Roth et al.		
TU	5	6073109		2000-06-06	Flores et al.		
TU	6	6871298	B1	2005-03-22	Cavanaugh et al.		
TU	7	5555432		1996-09-10	Hinton et al.		
TU	8	6145077		2000-11-07	Sidwell et al.		

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Attorney Docket Number	AUS920030483US1	

TTU	9	6865666	B2	2005-03-08	Yoshida et al.	
TTU	10	5590352		1996-12-31	Zuraski et al.	
TTU	11	6199204	B1	2001-03-06	Donohue	
TTU	12	6202207	B1	2001-03-13	Donohue	

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TTU	1	20050257092	A1	2005-11-17	Alexander et al.	
TTU	2	20050177822		2005-08-22	Kuch et al.	
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<sup>1</sup> See Kind Codes of USPTO Patent Documents at [www.USPTO.GOV](http://www.USPTO.GOV) or MPEP 901.04. <sup>2</sup> Enter office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>3</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>4</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. <sup>5</sup> Applicant is to place a check mark here if English language translation is attached.